

Quiz 4 Review Session

- Calling conventions
- Callee saved/caller saved registers

- Stack alignment

Compiler Toolchains

- Assemblers
- Linkers
- Loaders

Control Flow Graphs

- Basic Blocks
- Edge Types
- Dominators
- Static Single Assignment (SSA) Form
- Φ -nodes

Dataflow

- Dead Code Elimination
- Constant Folding
- Copy/Constant Propagation

Final Code Optimization

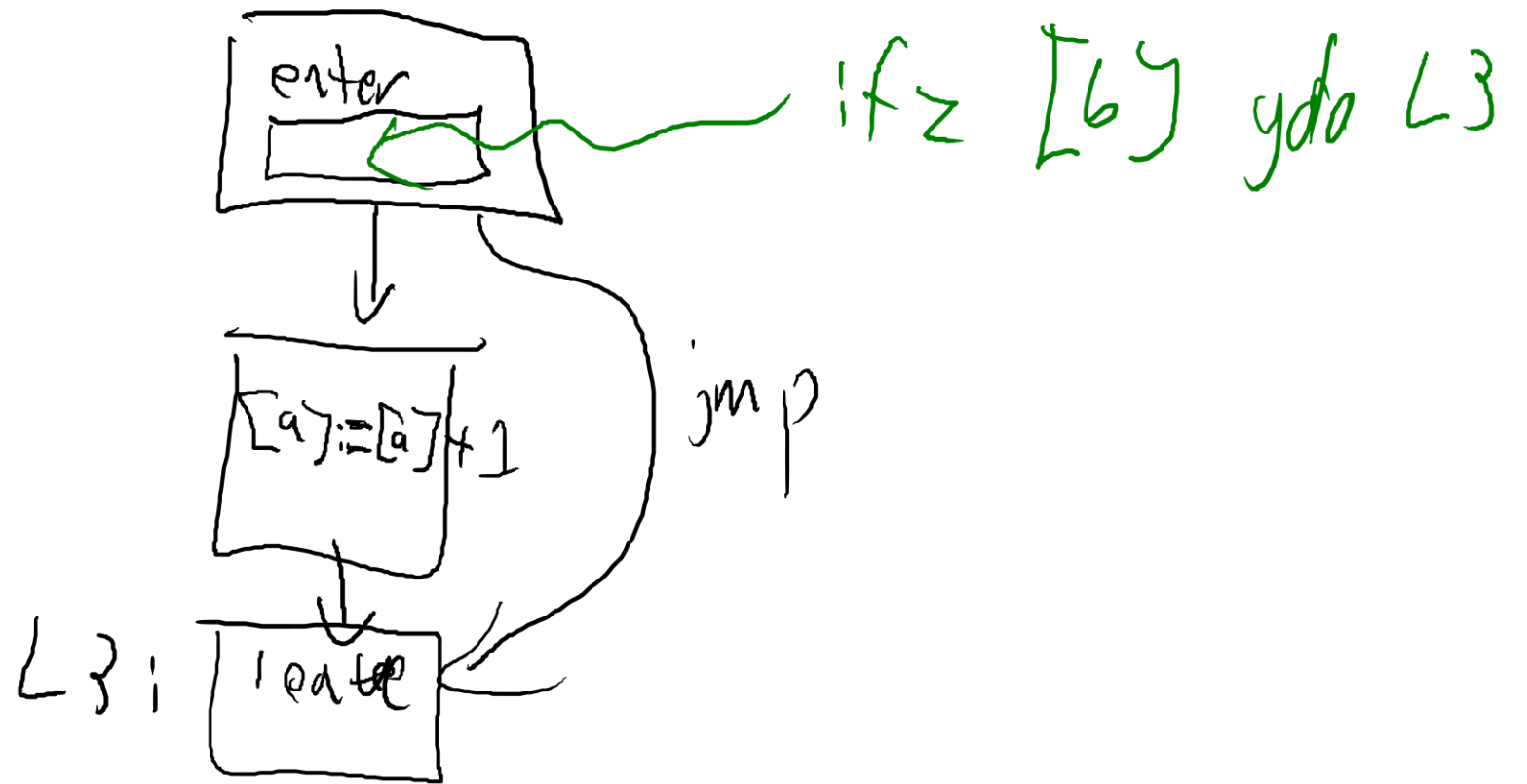
- Peephole optimization
- Interference graphs
- Register allocation
- Instruction selection
- Instruction strength reduction

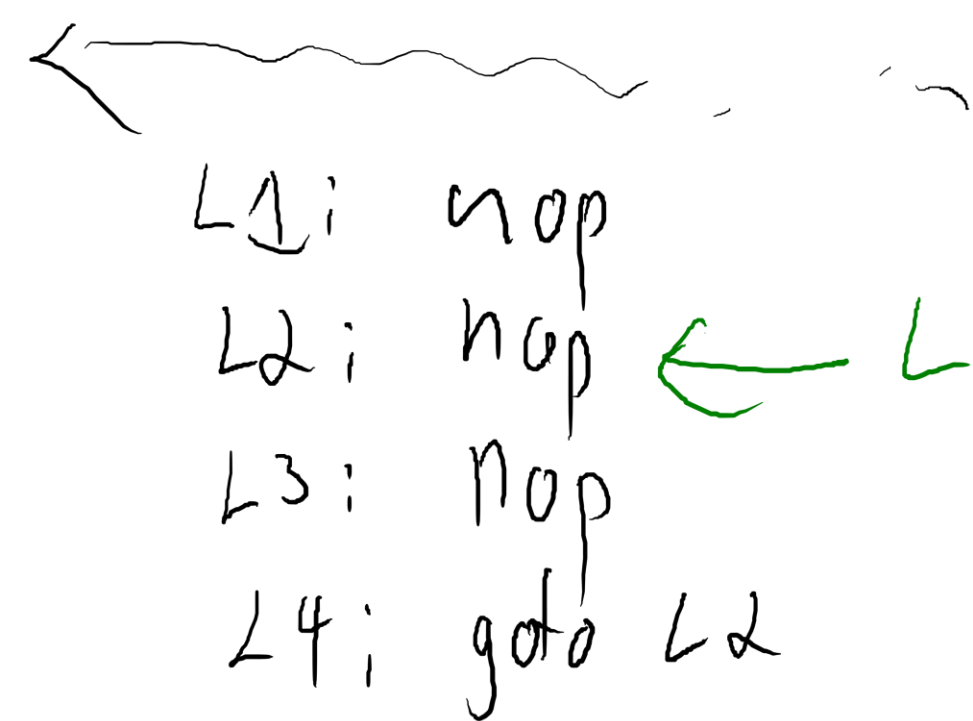
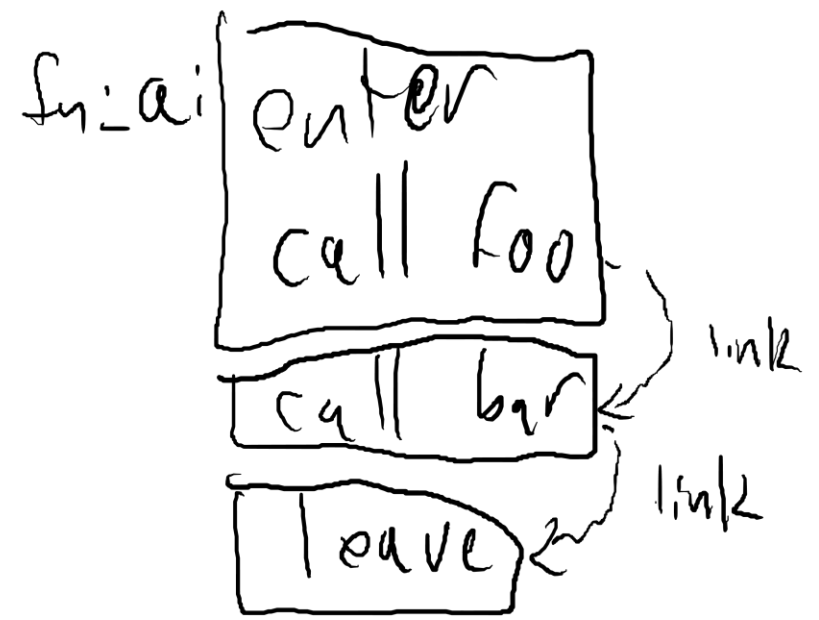
Heap Management

- Allocating Heap Memory
- Garbage Collection
 - Reference Counting
 - Mark and Sweep

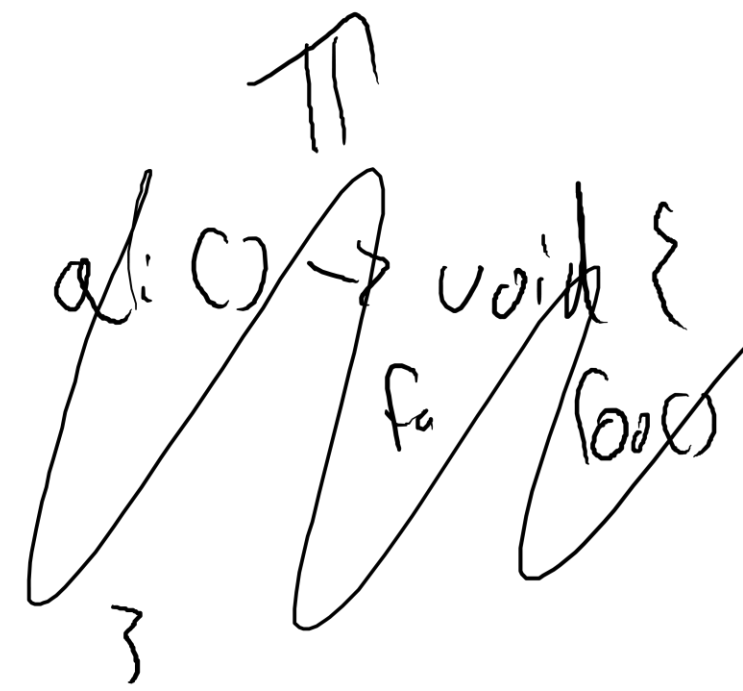
CFG

Overlay on a linear code repr.



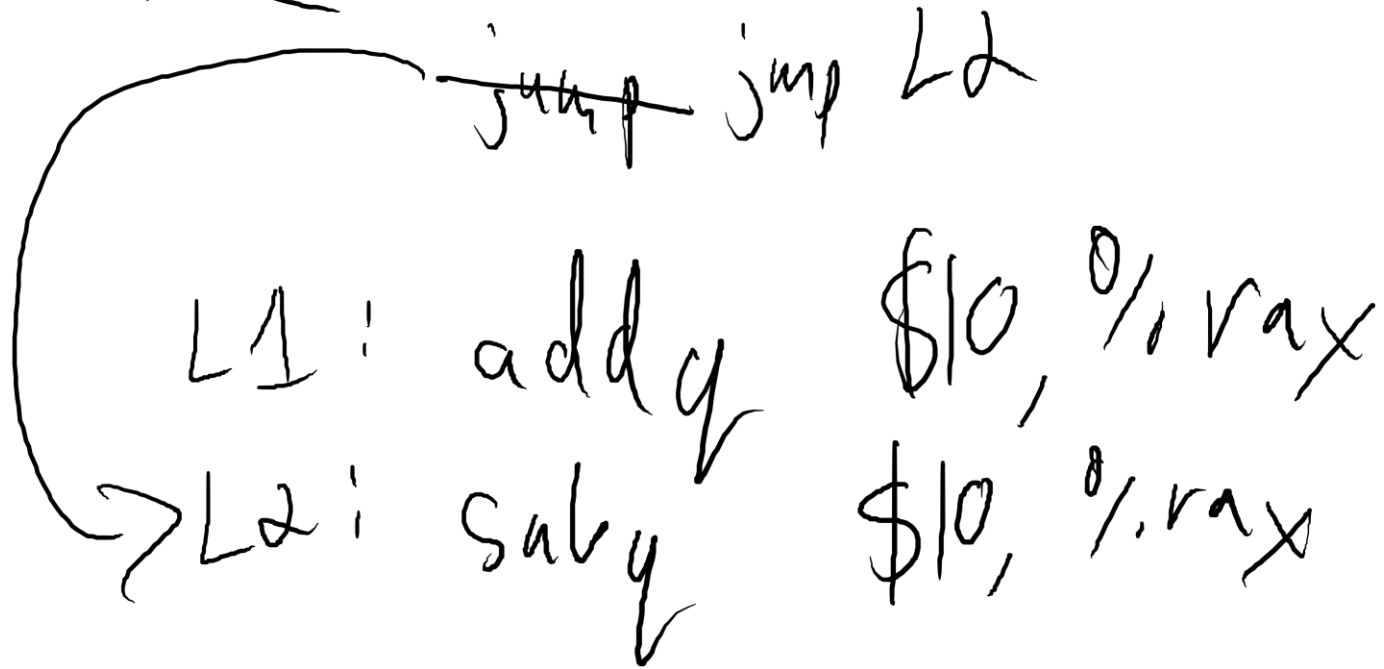


```
a: () -> void {
    foo();
    bar();
}
```



jump edge
full through edge
link edge

Perphole



Caller-Saved

may \$7, %rax

call bar

←

pushq %rax

≠

popq %rax

→ do not expect
to be \$7

Callee-Saved

may \$11, %r14

call foo

→ expect %r14 to
have \$11 in it

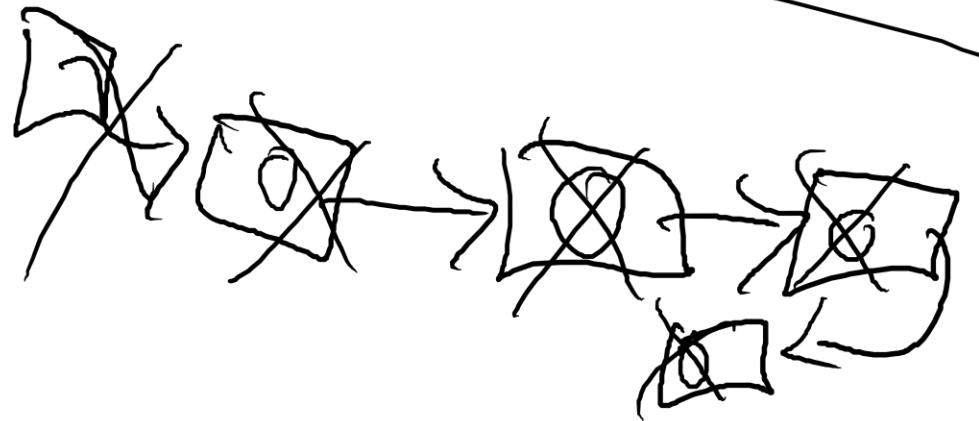
%rbp

Garbage Collection

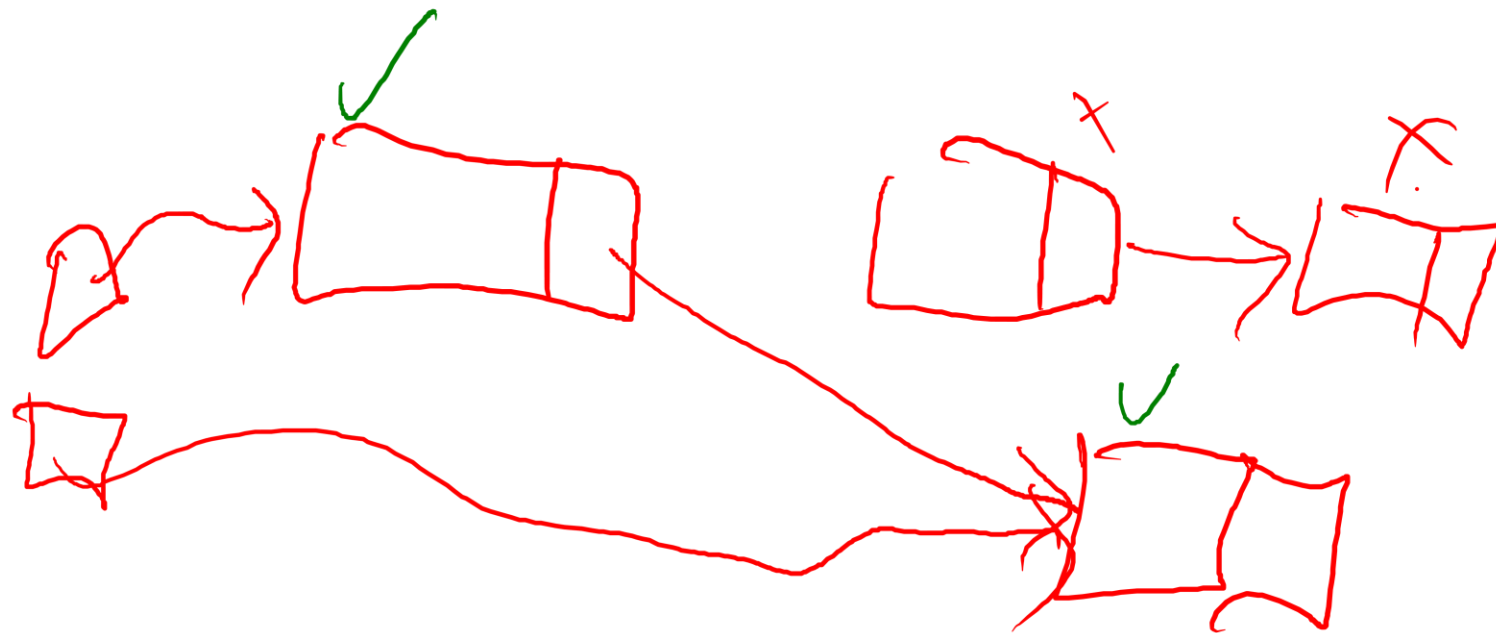
Automatic release of allocated memory at runtime

Naive reference counting

Mark and Sweep



Mark and Sweep



fn () {

<char* passwd = ...;

~~for (i = passwd; i < strlen(passwd); i++) {~~
~~passwd[i] = 0;~~

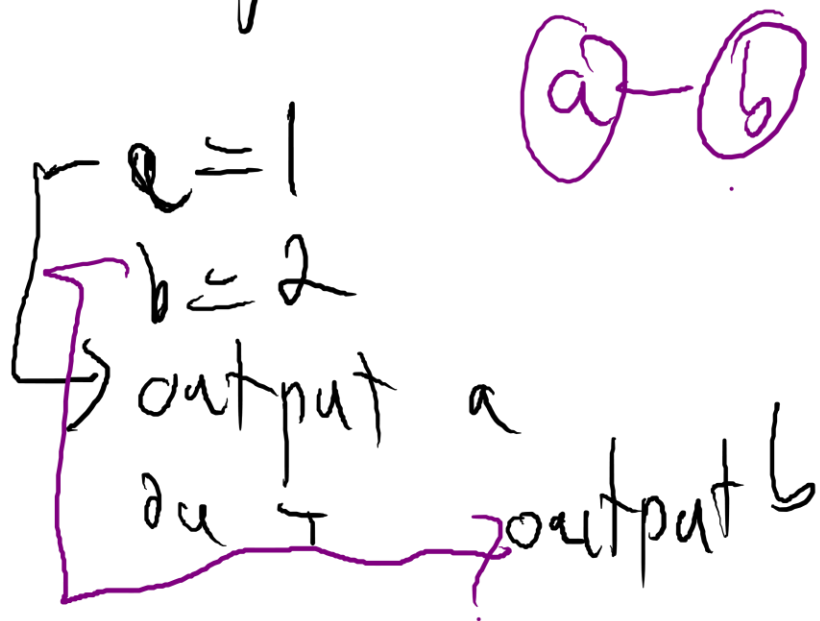
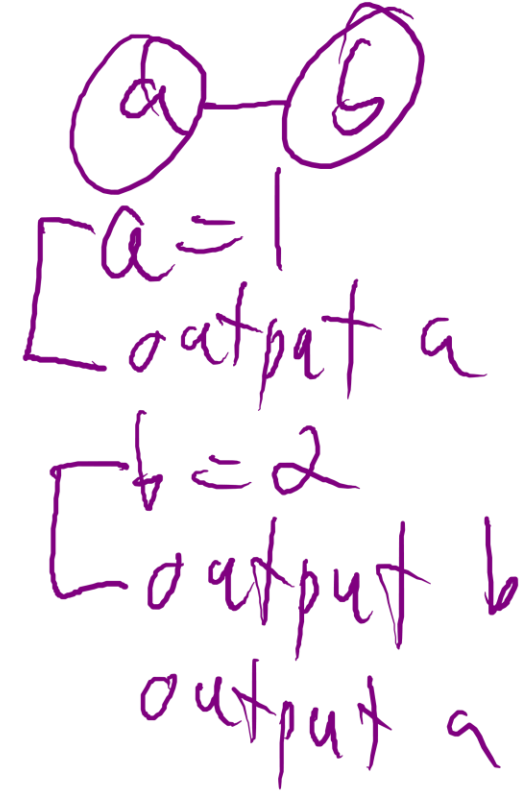
~~}~~

}

Interference Graph

Nodes: variables

Edges: simultaneous live uses



$a=1$
 $b=2$
return

